

IDS item:  
AN.

*[Signature]*

(IDS filed 7/2/7)

PAT-NO: JP410223530A

DOCUMENT-IDENTIFIER: JP 10223530 A

TITLE: MANUFACTURING METHOD FOR SEMICONDUCTOR DEVICE

PUBN-DATE: August 21, 1998

INVENTOR-INFORMATION:

NAME

YAMAZAKI, SHUNPEI

OTANI, HISASHI

ASSIGNEE-INFORMATION:

NAME

COUNTRY

SEMICONDUCTOR ENERGY LAB CO LTD

N/A

APPL-NO: JP09040141

APPL-DATE: February 7, 1997

INT-CL (IPC): H01L021/20, H01L021/322 , H01L029/786 , H01L021/336

ABSTRACT:

PROBLEM TO BE SOLVED: To provide a method of manufacturing a semiconductor device at high throughput.

SOLUTION: For adding a catalytic element for accelerating the crystallization of an amorphous Si film 102, an ion implanting method is utilized wherein the areas occupied by catalytic element-adding regions 107, 108 are reduced to increase the area of a crystal Si film available for TFTs, thereby increasing the degree of freedom of the circuit design. The step of crystallizing the Si film and gettering step for the catalytic element are conducted successively to greatly improve the throughput.

COPYRIGHT: (C)1998,JPO